

WHO WE ARE

We develop mixed signal CMOS IP and offer IC design services for emerging technology markets. We work with top tier Fabs and industry partners to bring best in class design solutions for fast market lead times.

CONTACT US

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Mixed Signal IC Design



UltraPhy

CMOS emerging technologies

KEY EXPERTISE

- Extensive experience for Wireless RFIC, mmWAVE, Power management & Sensor applications design
- Custom open access ready design flow for cost reduction
- Innovative low power, optimal cost IP development
- Extensive Industry experience for high quality time critical projects
- We work with our clients to deliver cutting edge solutions



OUR PRODUCTS & SERVICES

CMOS IP



We develop extensive range of IP for emerging wideband RF front end solutions to data acquisition solutions such as delta sigma and SAR ADC. We also prepare oscillator and PLL IPs along with innovative jitter reduction solutions.

Design Services



We have track record of working with top tier technology developers for wireless, automotive, sensors and industrial applications. We are happy to engage in feasibility to architecture design and delivery of solution including preparing statement of work.

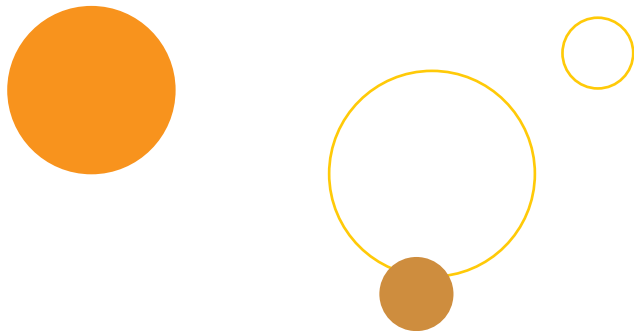
ASIC



With increased complexity for ASIC design and functionality comes the need to have thorough verification of mixed signal design along with costing of IP and careful planning development process. We tackle all aspects of ASIC development process to ensure complete piece of mind.

AVAILABLE IP

- 10 Bit general purpose, low-power SAR ADC
- GHz Fractional N PLL with on chip loop filter
- Ultra-high-speed comparators
- Rail to rail amplifiers (sub-threshold and inversion mode)
- PWM Driver amplifier controllers
- Jitter-Reduction IP for clock distribution applications in mmwave, RF, AI and GPU applications
- PM: Bandgaps, PTAT, CTAT, VBGR, current generators, LDOS
- Energy Harvesting



MARKETS / PROJECTS

Bio Medical Wearable

Working on life critical cochlear implant was challenging but a rewarding project (youngest user of clients application at 6 months age) with fist pass analog processing ASIC design achieved on time and budget.

Consumer & Industrial

Infotainment system requiring high SNR drivers or industrial drivers with high voltage drive capabilities, these are complex high performance IPs that require low-power design proficiency. Having delivered these challenging requirements was great for customer satisfaction.

Communications and Sensors

Designing wideband wireless sensors along with multi-GHz RF calibration mechanism was interesting project. We conducted initial feasibility for the architecture and then prepared statement of work to deliver design of silicon.



MISSION

“WE DEVELOP CMOS IP FOR EMERGING TECHNOLOGY MARKETS”

- Best in class design services by industry experts
- Track record of first pass silicon
- Optimal cost solutions
- Production grade silicon IP
- Fast turnaround times for silicon development

With our extensive experience of varied IC application development we bring valuable expertise to market to save time and provide sound solutions for analog, digital and mixed signal design technology challenges.

We look forward to working with our customers to bring solutions to their IP and ASIC needs.

